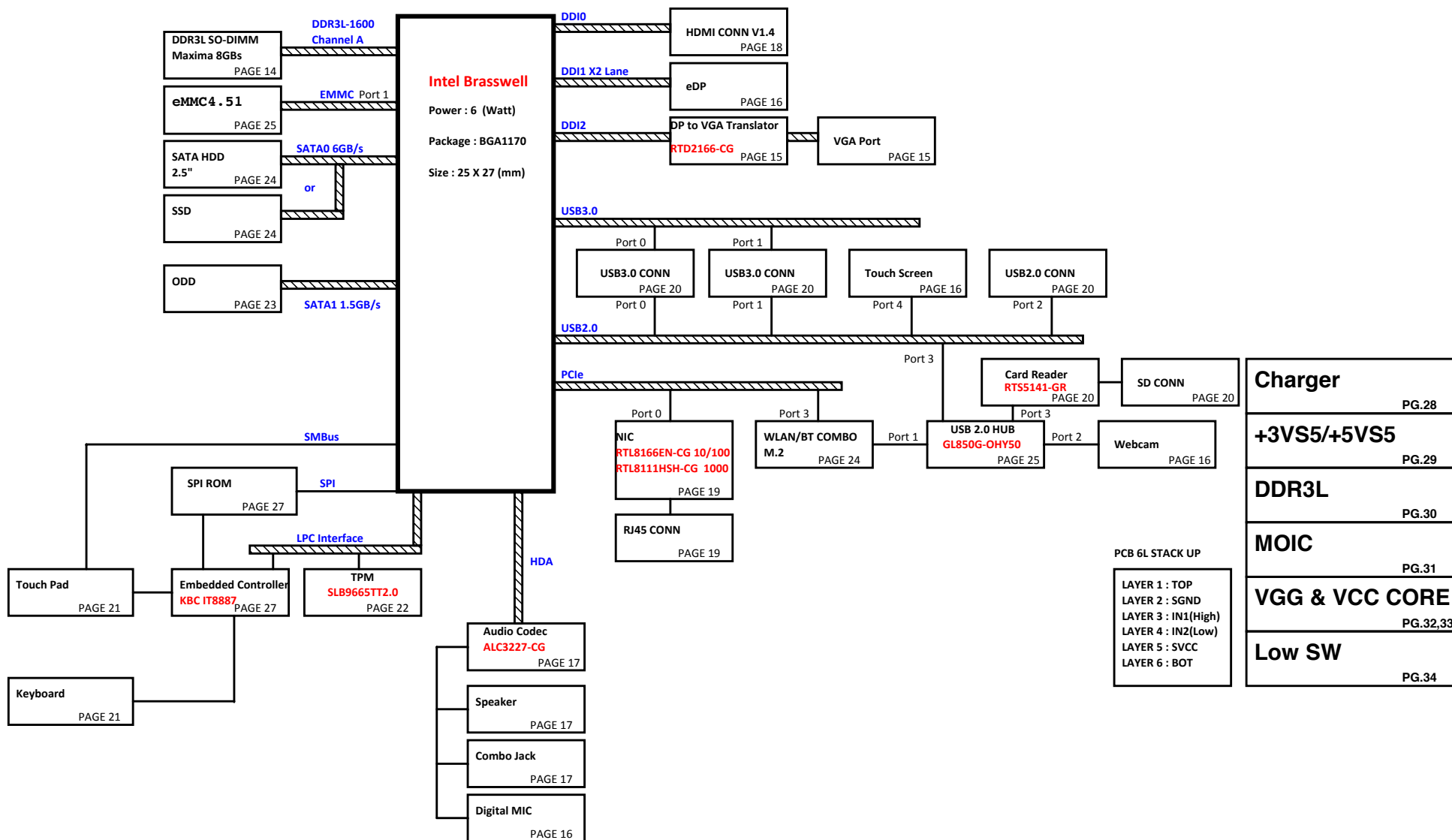
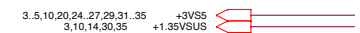
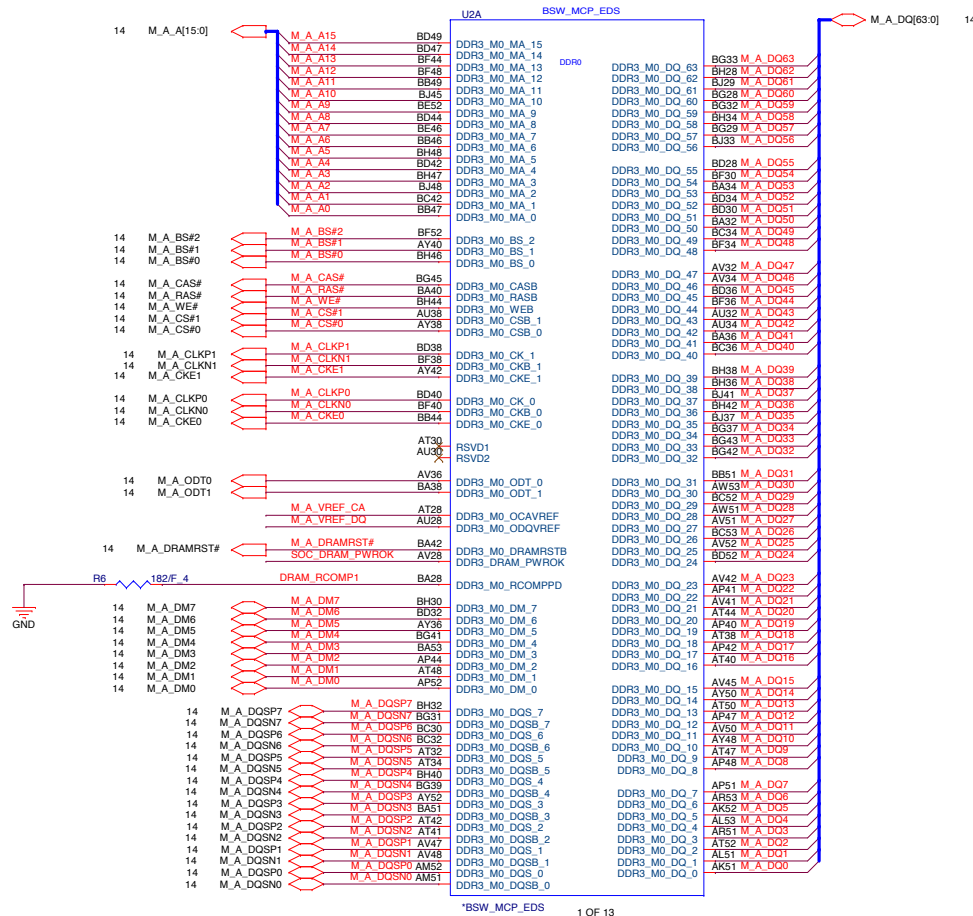
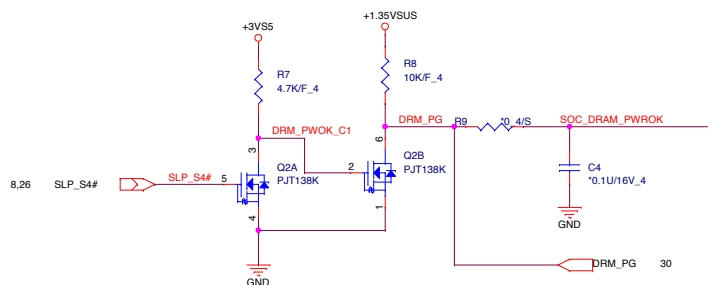
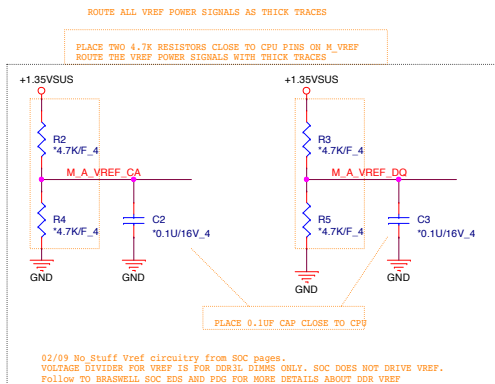


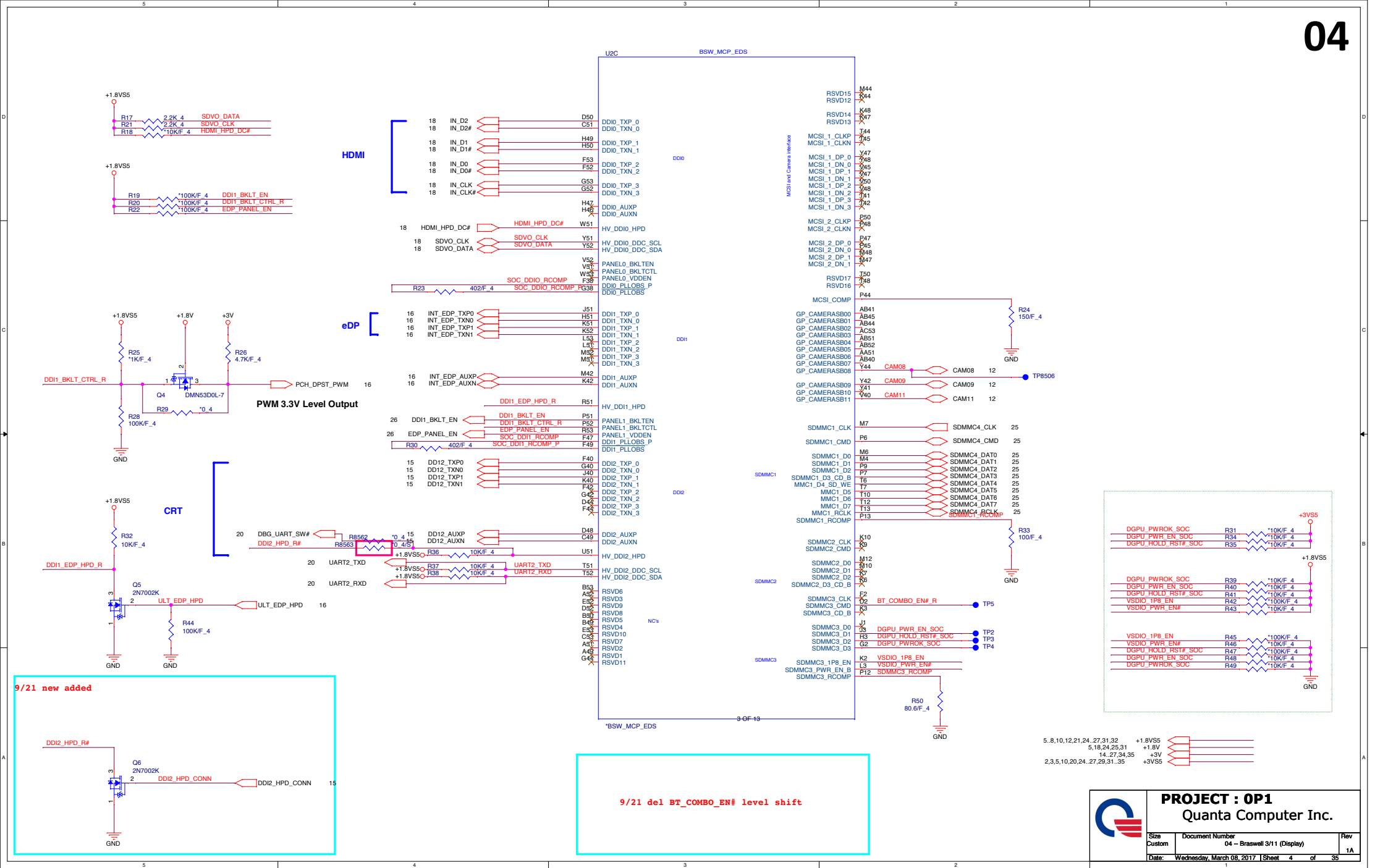
NFL-C BSW 14"

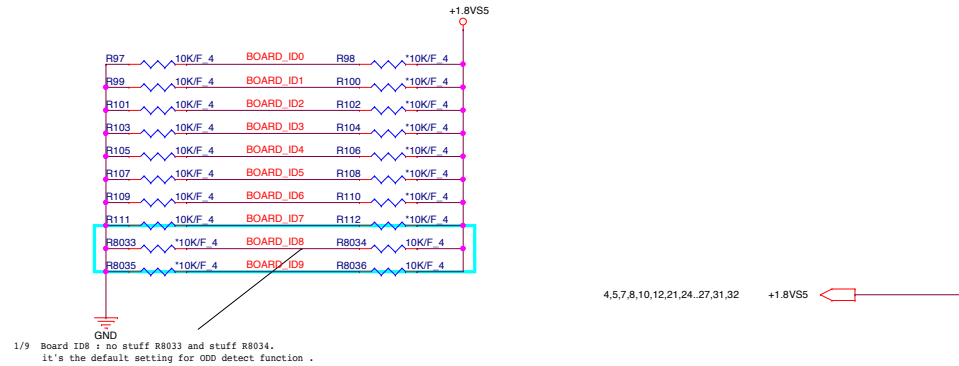
Intel Braswell-M Platform Block Diagram









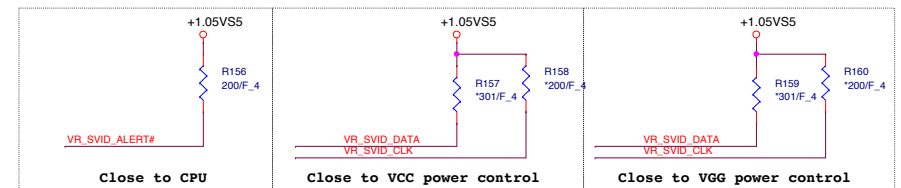


BOARD_ID7	BOARD_ID6	BOARD_ID5	Vender	Quanta PN	Description
0	0	0	Hynix-2GB	AKD5PGSTW14	IC SDRAM (96P) H5TC4G63CFR-PBA TOPBSQ
0	0	1	Micron-2GB		
0	1	0	Samsung-2GB Q Dye		
0	1	1	Samsung-2GB E Dye	AKD5JG0T503	C SDRAM(96P)K4B4G1646E-BYK0(FBGA)TOPBSQ
1	0	0			
1	0	1	Micron-2GB P Dye	AKD59GSLT04	C SDRAM(96P) MT41K256M16TW-107:P TOPBSQ
1	1	0			
1	1	1			

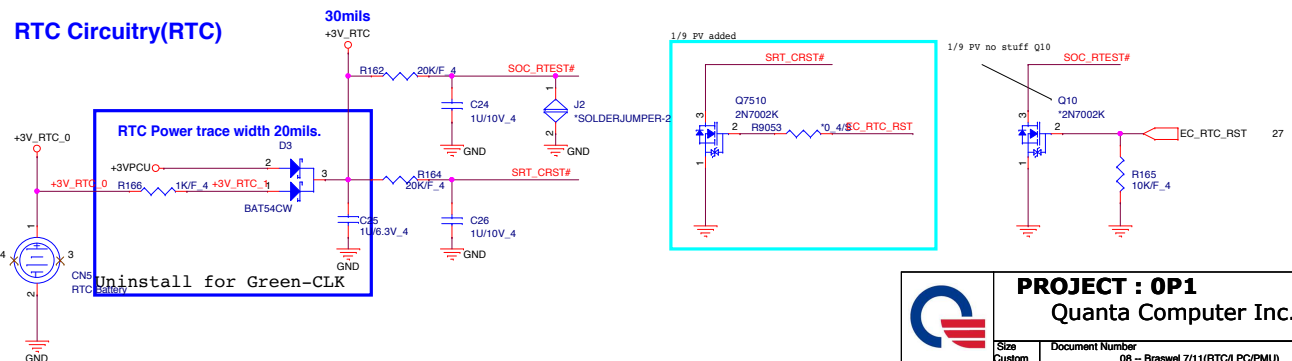
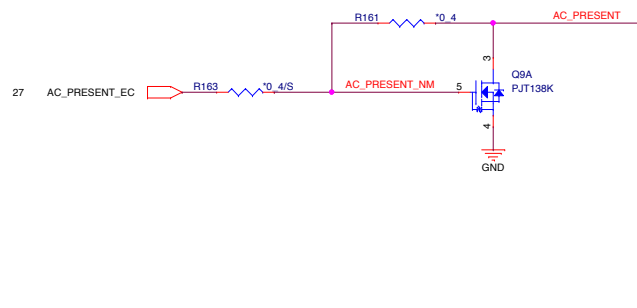
1/9 Board ID8 : no stuff R8033 and stuff R8034.
it's the default setting for ODD detect function .




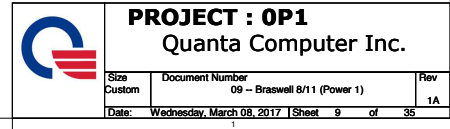
The schematic shows the internal components of the RTC module. It includes two capacitors, C18 and C20, both specified as 15pF/50V. A crystal Y2 is connected between pins RTC_X1 and RTC_X2, with a frequency of 32.768KHZ/20ppm. The RTC module is powered by a 1.8VSS supply through a network of resistors: R131 (10K/F), R132 (10K/F), R135 (2.2K), R138 (1K/F), R140 (20K/F), R142 (10K/F), R145 (10K/F), R149 (2.2K), R153 (100K/F), R154 (10K/F), and R155 (100K/F). A note indicates that C18 and C20 should be changed to 15pF.



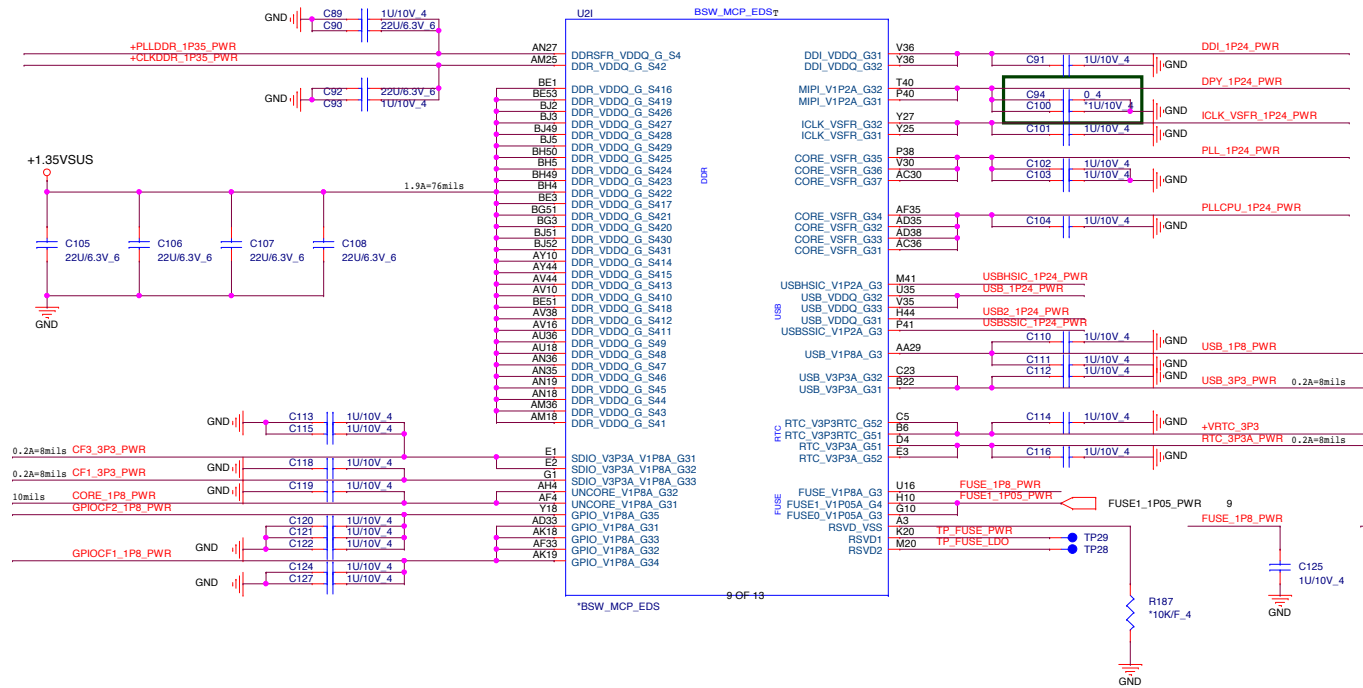
AC Present: This input pin indicates when the platform is plugged into AC power.



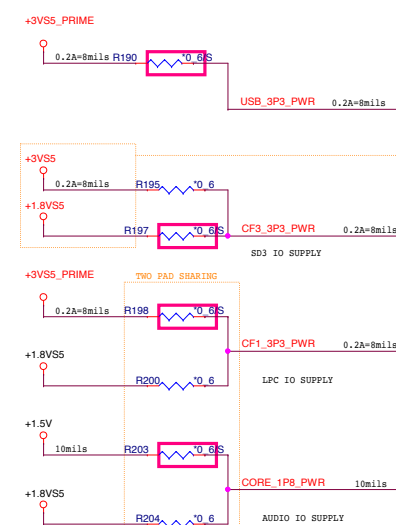
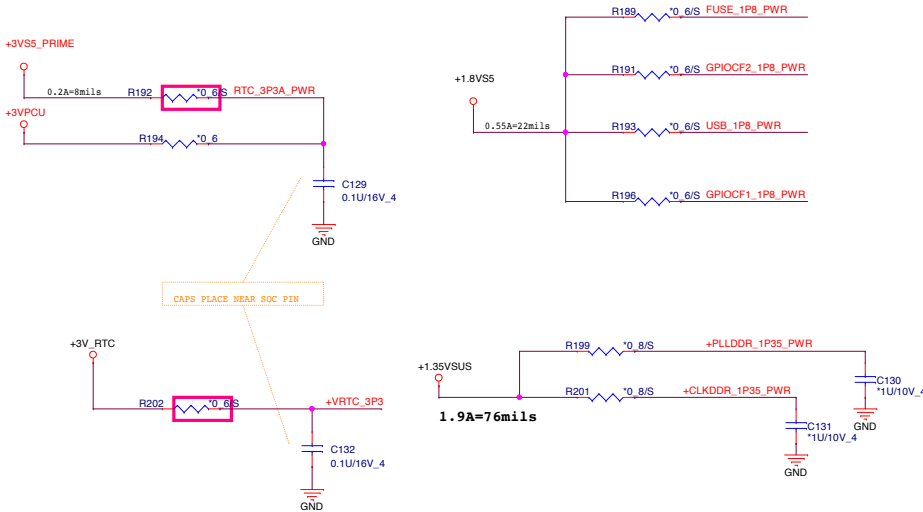
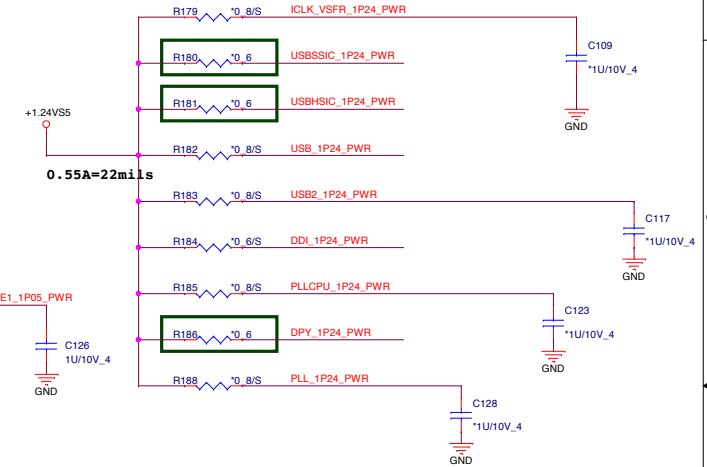
	PROJECT : 0P1 Quanta Computer Inc.		
	Size Custom	Document Number 08 - Braswel 7/11(RTC/LPC/PMU)	Rev 1A
	Date: Wednesday, March 08, 2017	Sheet 6 of 35	



PLACE THE CAPS UNDER THE PKG SHADOW

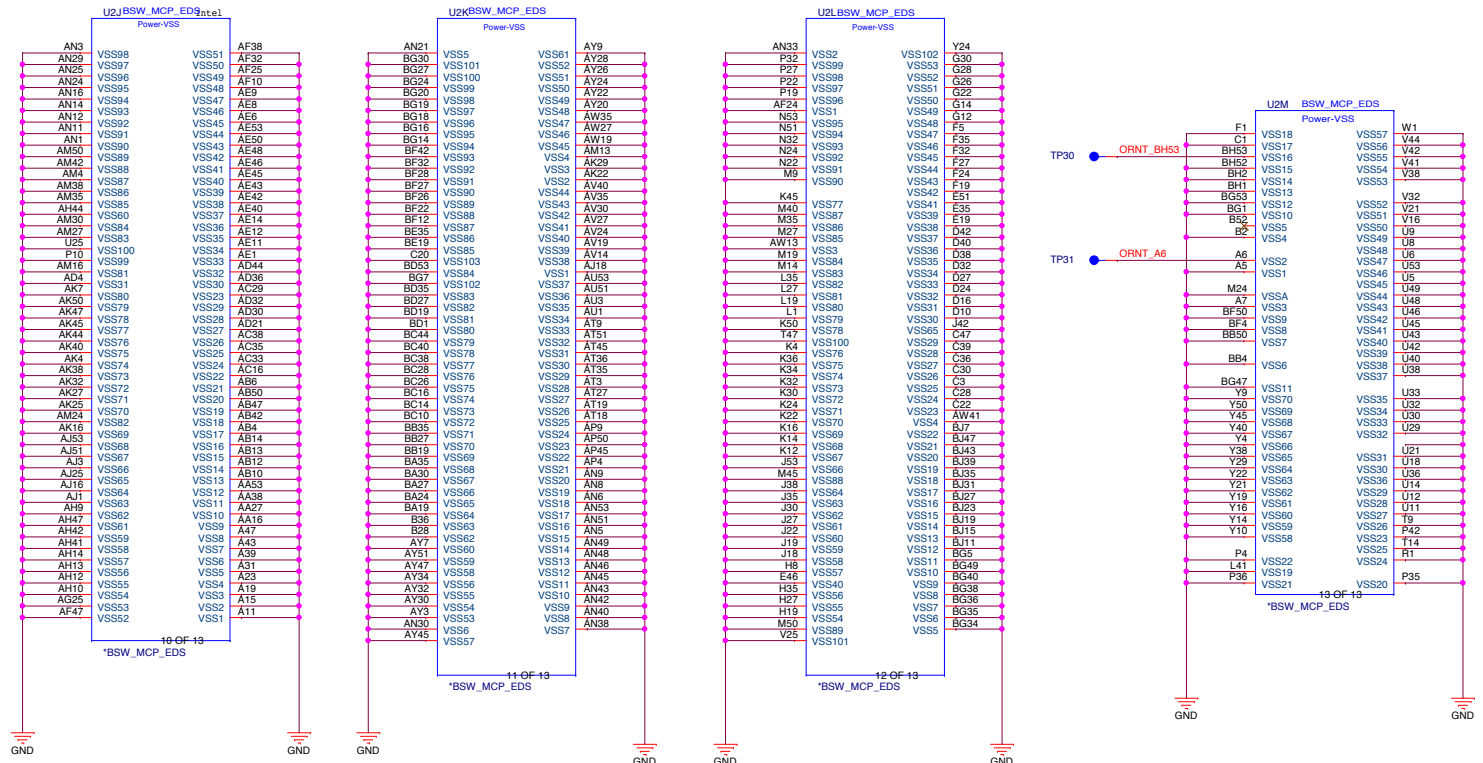


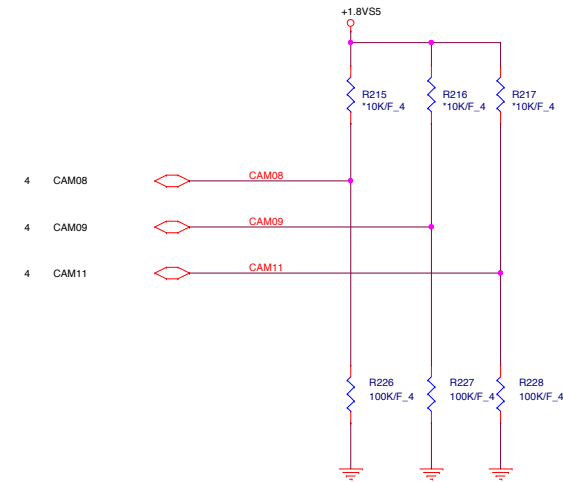
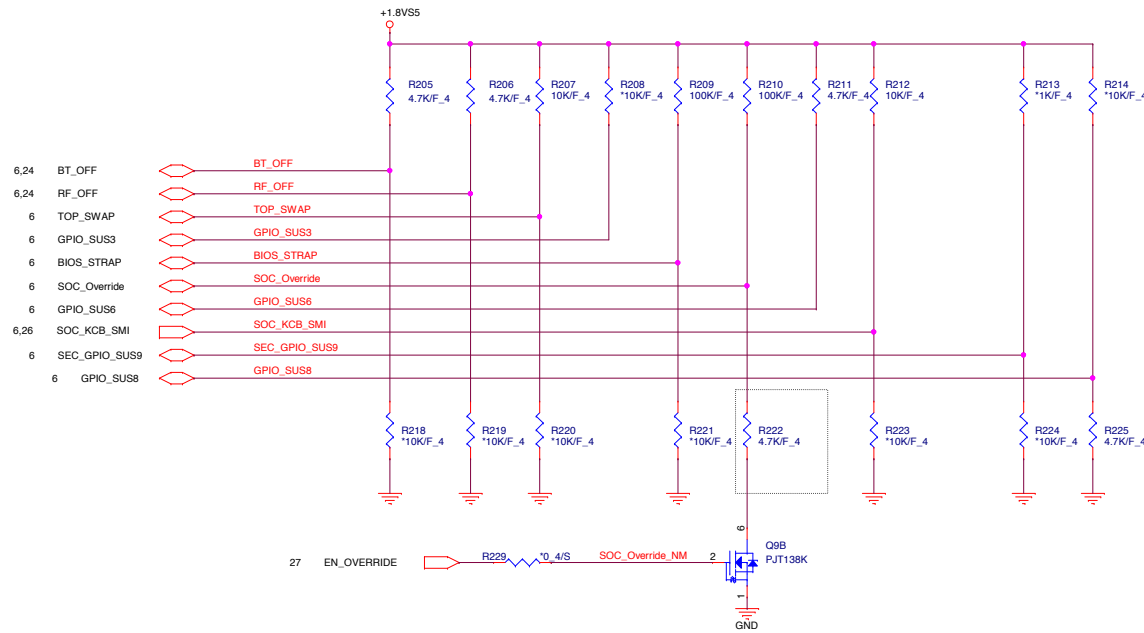
PV: Disable HSIC SSIC MIPI power.



SDMMC3_PWR_EN_N	SDMMC3_1P8_EN	VSDIO (V)
1	0	0V
1	1	0V
0	0	3.3V
0	1	1.8V

4,8,12,21,24,27,31,32	+1.8VS5
9,31,33	+1.0VS5
9,31	+1.1VS5
31	+1.2VS5
27,31	+3VSS_PRIME
2,5,20,24,27,29,31,35	+3VS5
2,3,14,30,35	+1.35VSUS
8	+3V_RTC
8,20,21,24,26,29	+3VPCU






REQUIRED STRAPS

	GPIO_SUS0	GPIO_SUS1	TOP_SWAP	GPIO_SUS3	BIOS_STRAP	SOC_Override	GPIO_SUS6	SOC_KCB_SMI	GPIO_SUS8
PULL HIGH	DDI0 detected DEFAULT	DDI1 detected DEFAULT	Normal Operation DEFAULT	Reserve 10 KQ PU DEFAULT	SPI DEFAULT	Normal Operation DEFAULT 20150209 PV change	10 KQ PU to 1.8V DEFAULT	Reserve 10 KQ PU DEFAULT	Supply is 1.35V
PULL LOW	DDI0 not detected	DDI1 not detected	Change Boot Loader address		LPC	Override			Supply is 1.25V DEFAULT

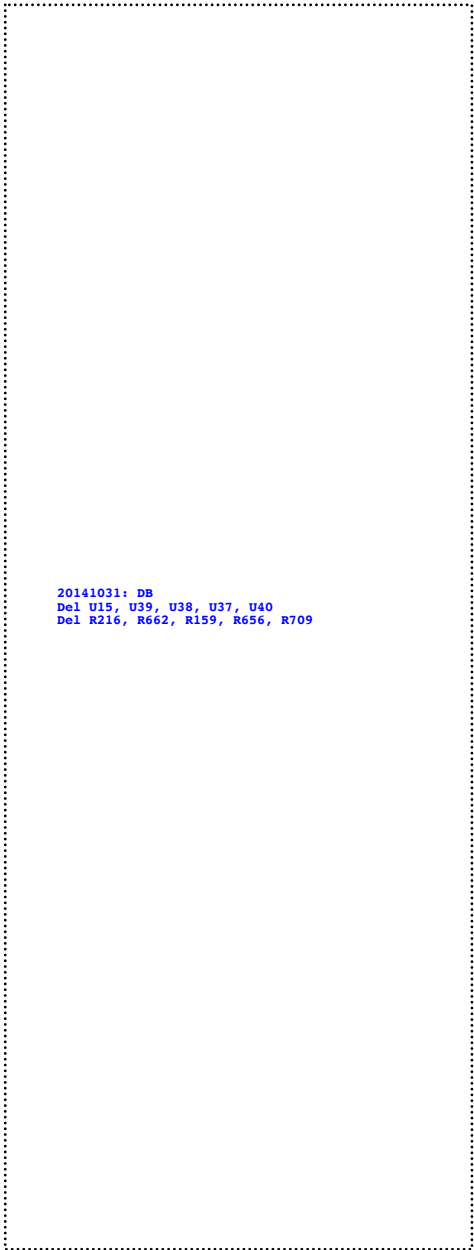
	CAM08	CAM09	CAM11
PULL HIGH	ICLK Xtal OSC Bypass	CCU SUS RO Bypass	RTC OSC Bypass
PULL LOW	ICLK Xtal OSC No Bypass DEFAULT	CCU SUS RO No Bypass DEFAULT	RTC OSC No Bypass DEFAULT

4,8,10,21,24,27,31,32 +1.8VSS5

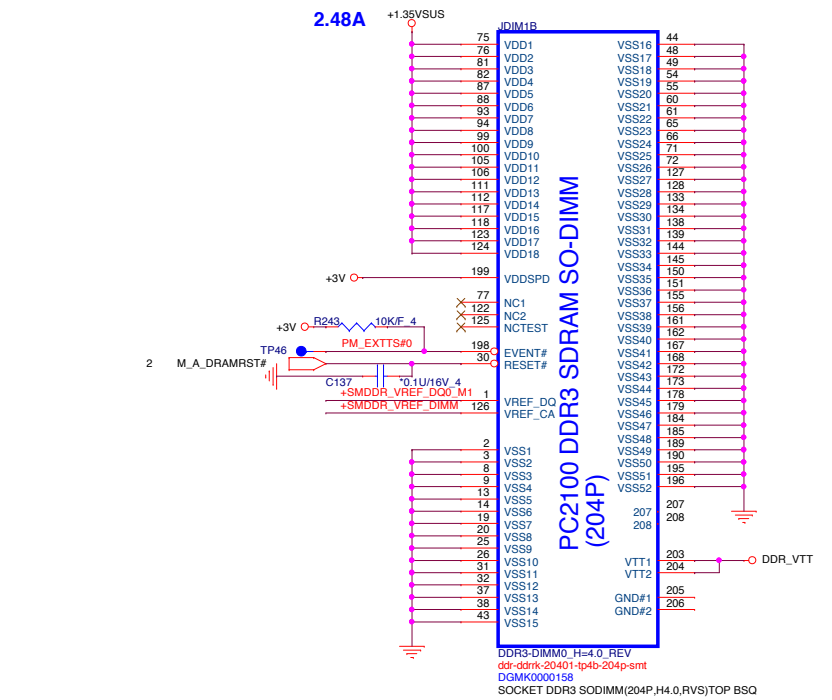
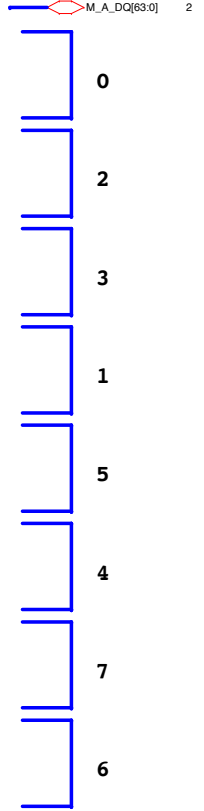
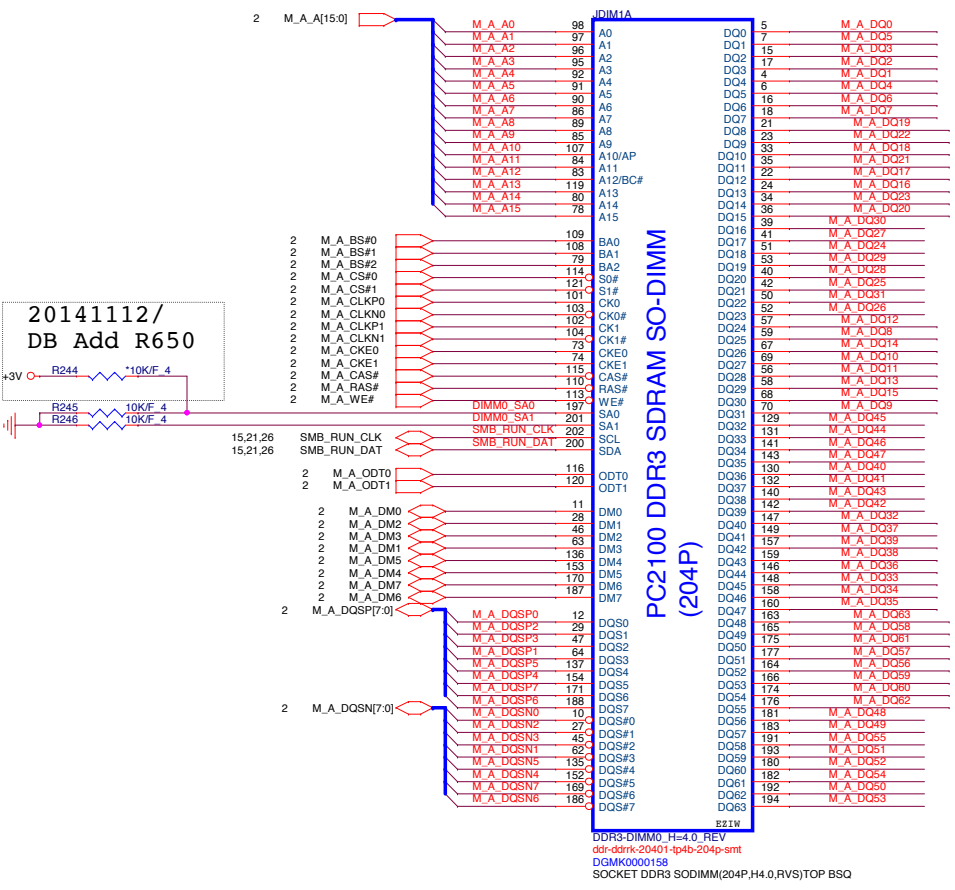


PROJECT : 0P1
Quanta Computer Inc.

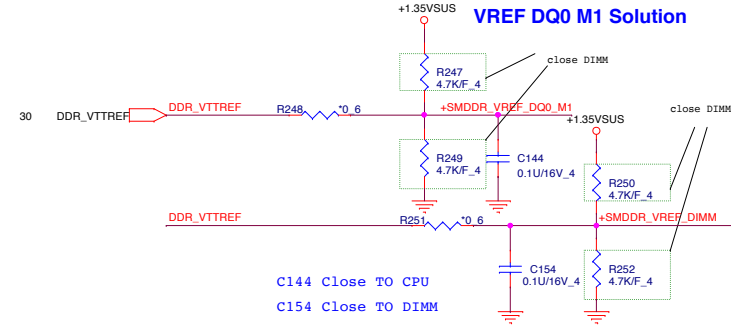
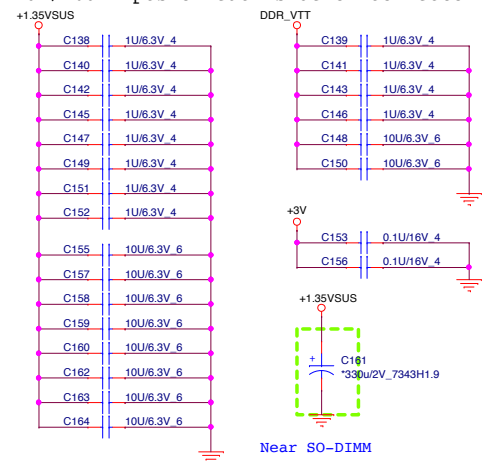
Size Custom	Document Number 12 -- Braswell 11/11 (Strap)	Rev 1A
Date: Wednesday, March 08, 2017 Sheet 12 of 35		



20141031: DB
Del U15, U39, U38, U37, U40
Del R216, R662, R159, R656, R709



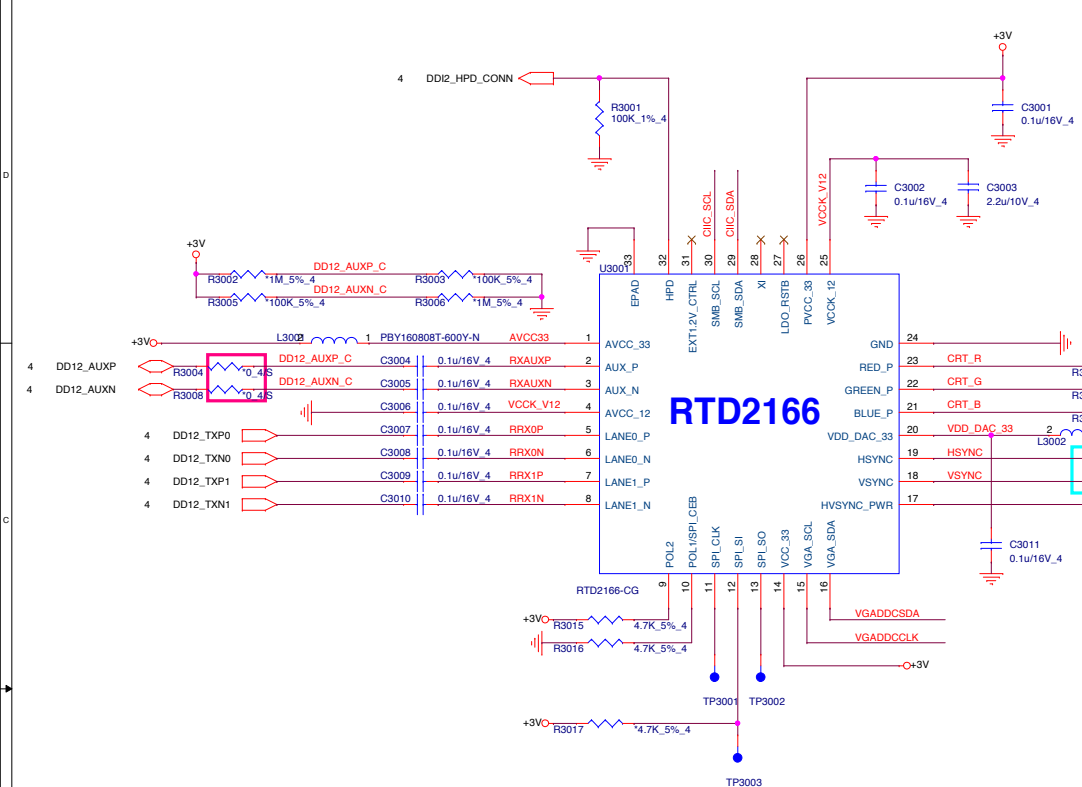
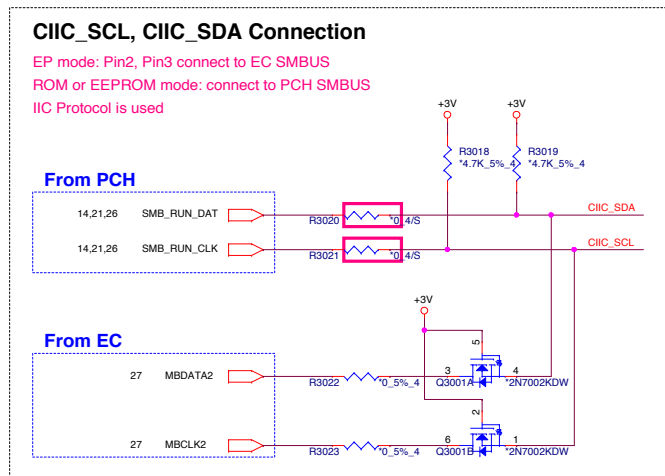
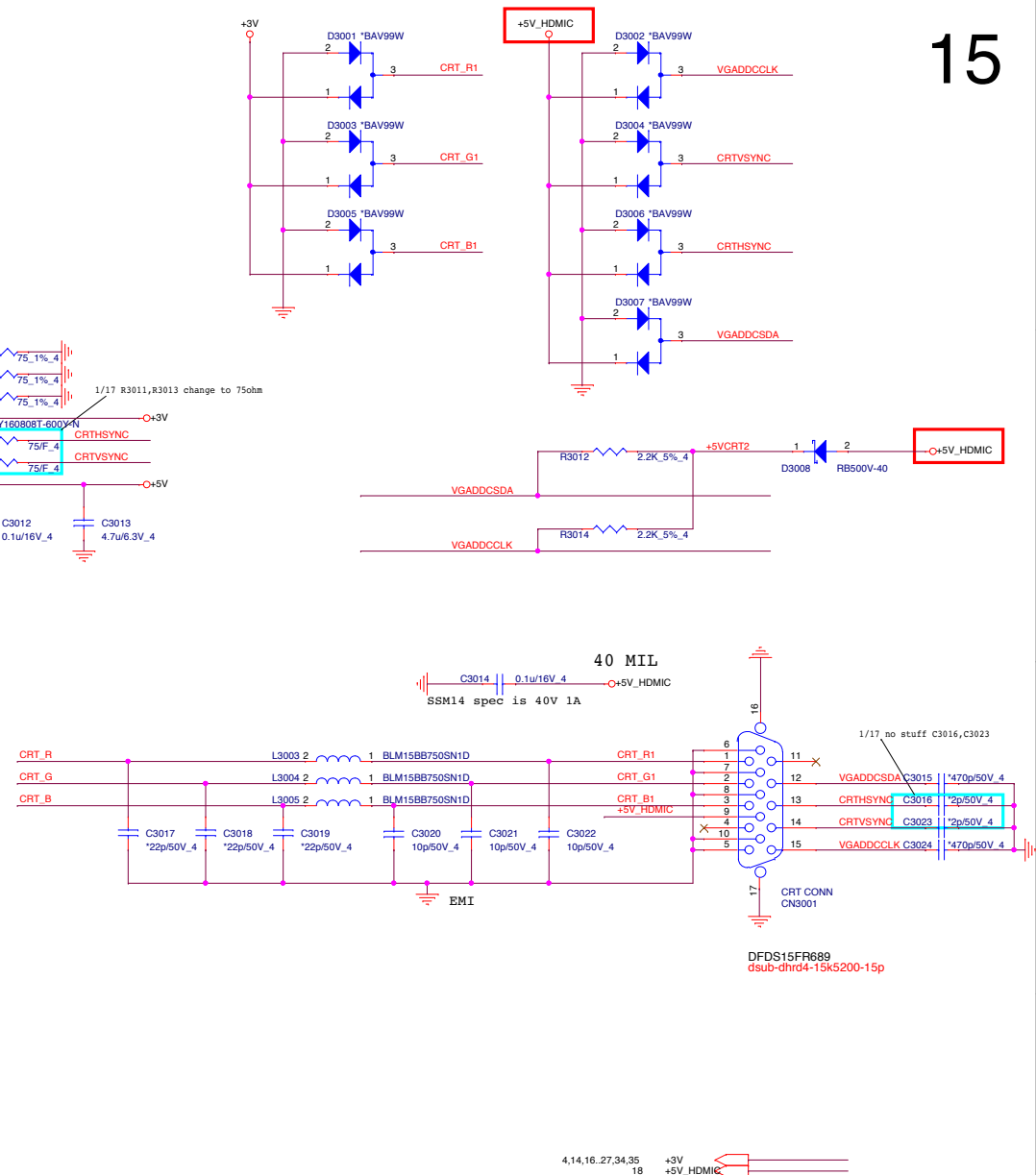
Place these Caps near So-Dimm0.
1uF/10uF 4pcs on each side of connector



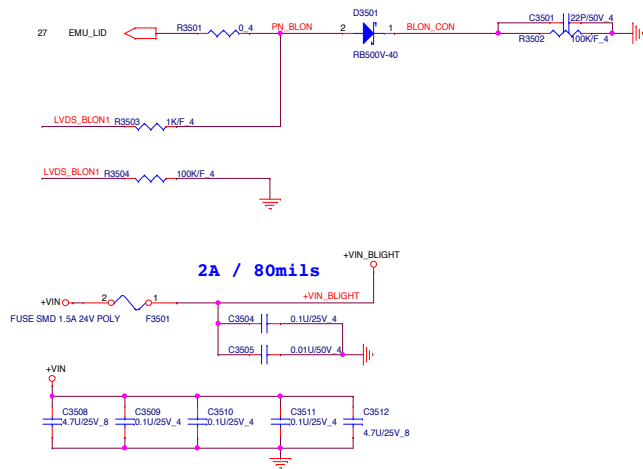


PROJECT : OP1
Quanta Computer Inc.

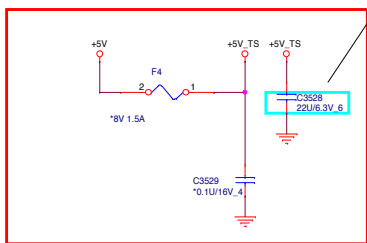
Size Custom	Document Number 15 -- DP2CRT	Rev 1A
Date:	Wednesday, March 08, 2017	Sheet 15 of 35



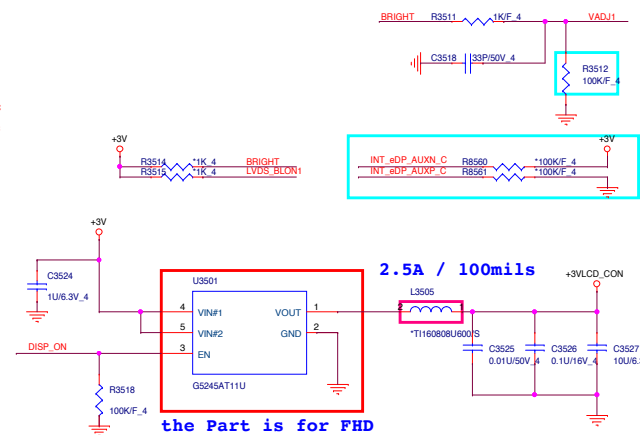
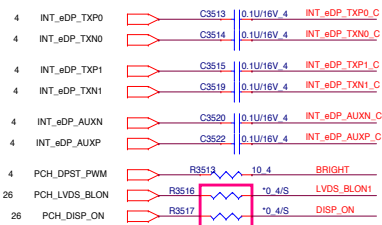
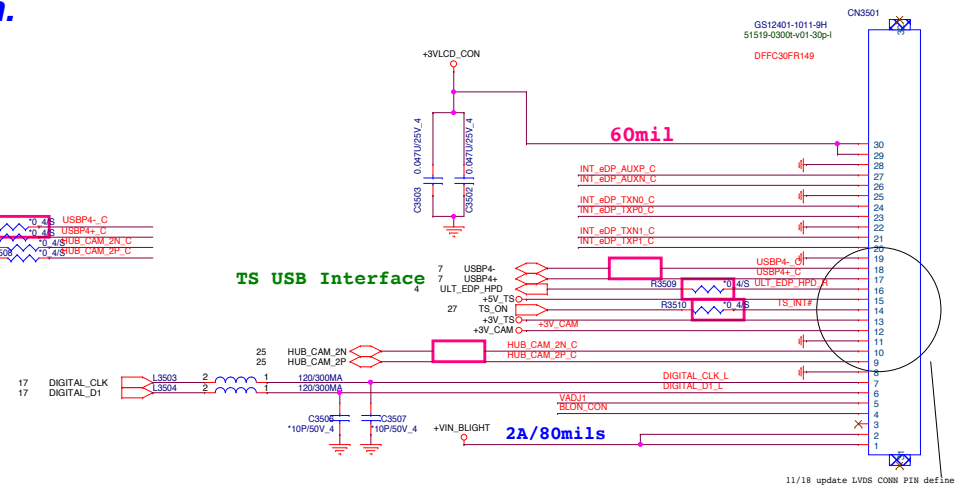
LID Switch




1/9 PV Stuff C3517,C3521,C3528 and C3516 used 10p

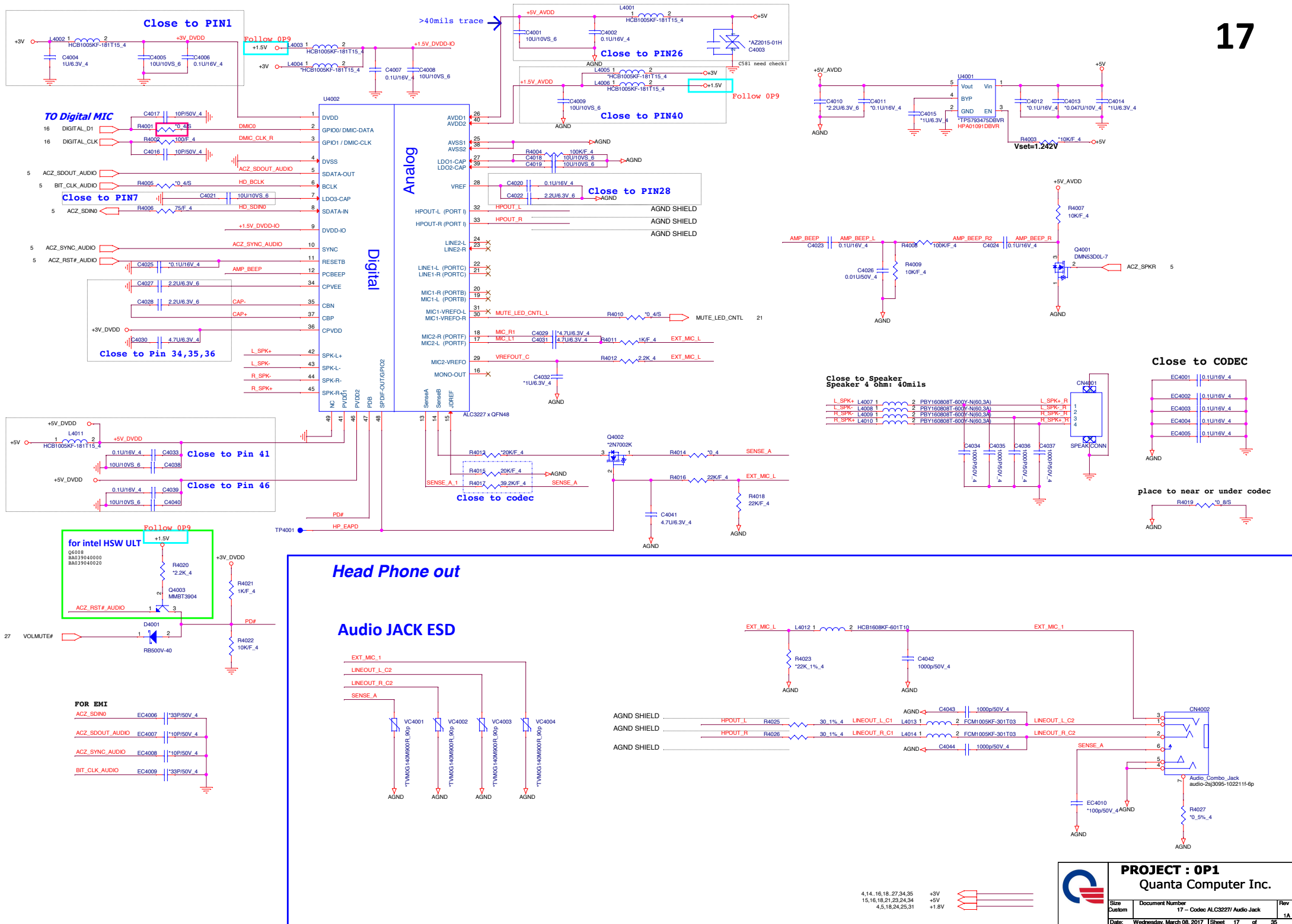


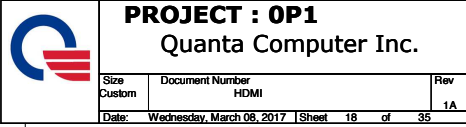
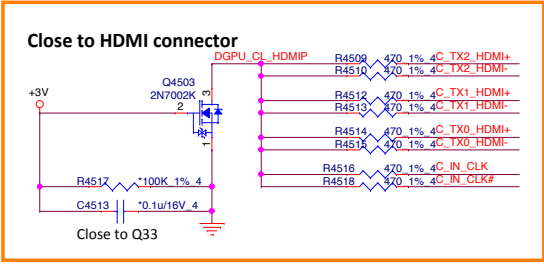
eDP Conn.

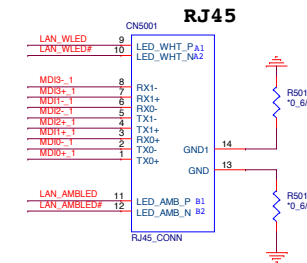
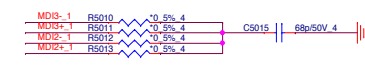
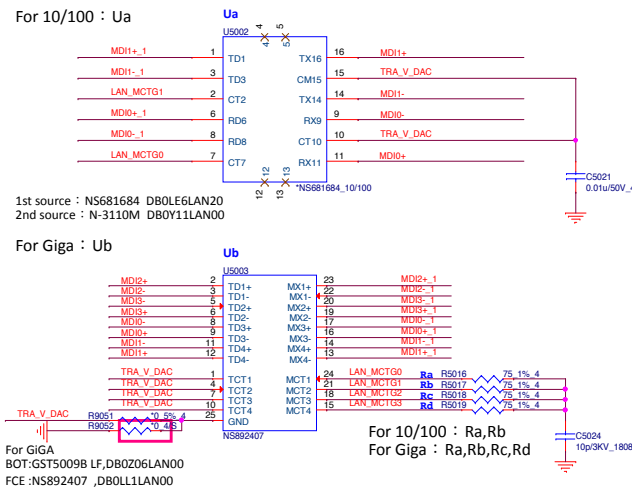
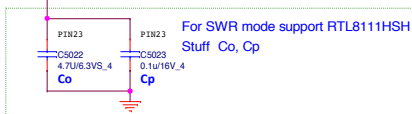
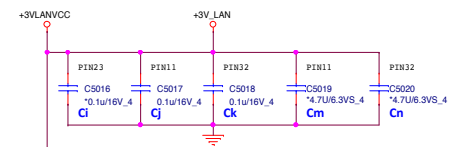
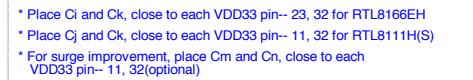
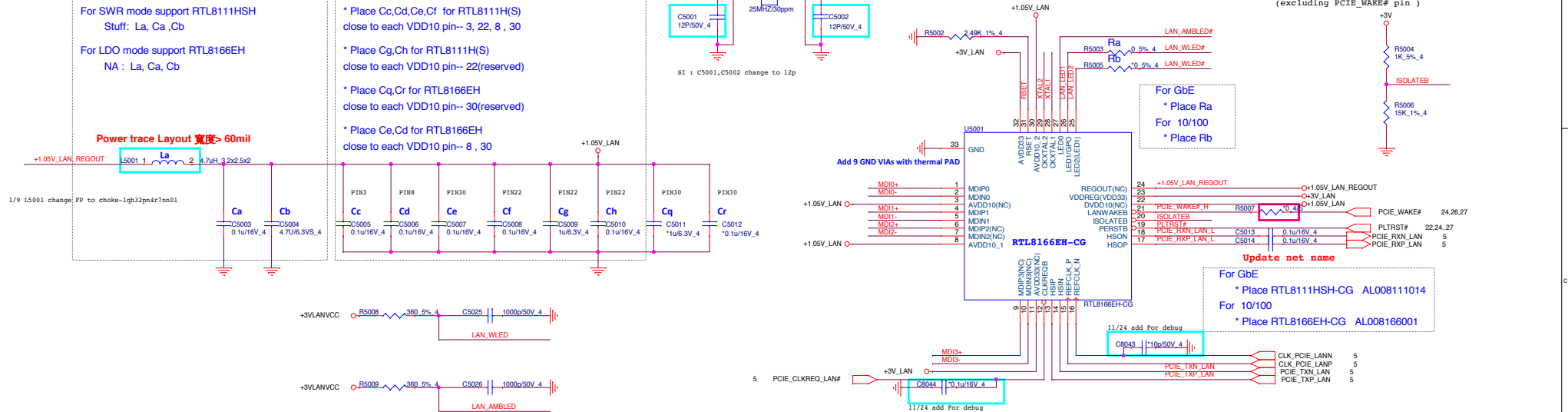


the Part is for FHD

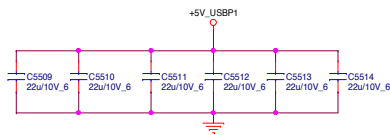
	PROJECT : 0P1 Quanta Computer Inc.		
	Size Custom	Document Number 16 - eOP CONN/VID/CAM/D-MIC/T/S	Rev 1A
	Date: Wednesday, March 08, 2017	Sheet 16 of 35	



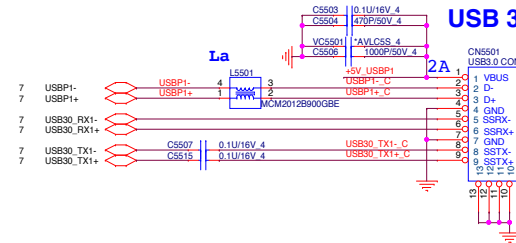




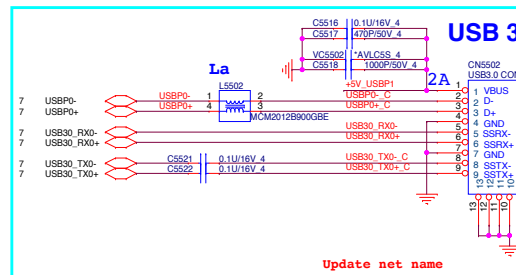
For 10/100 : ESD1
For Giga : ESD1,ESD2



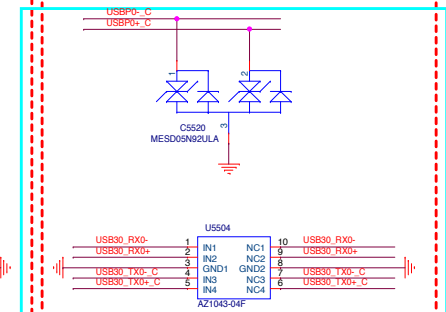
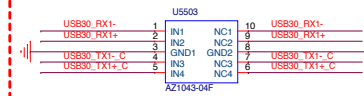
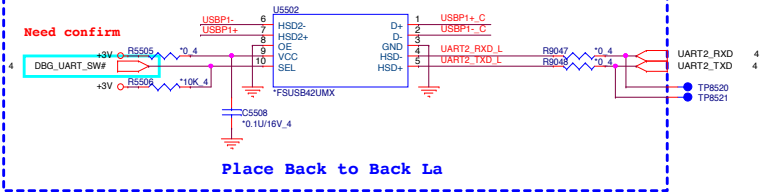
USB 3.0



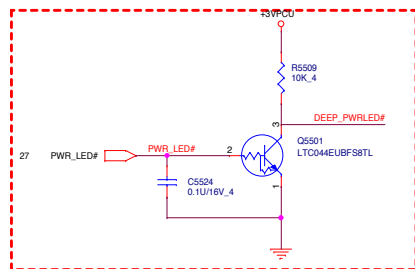
USB 3.0



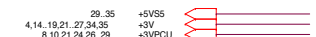
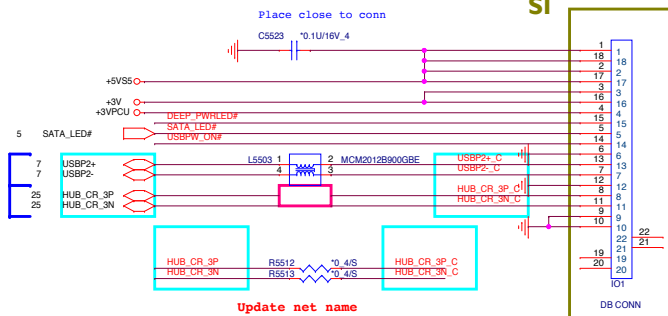
Need confirm



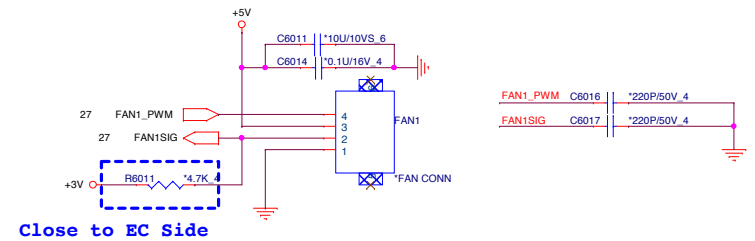
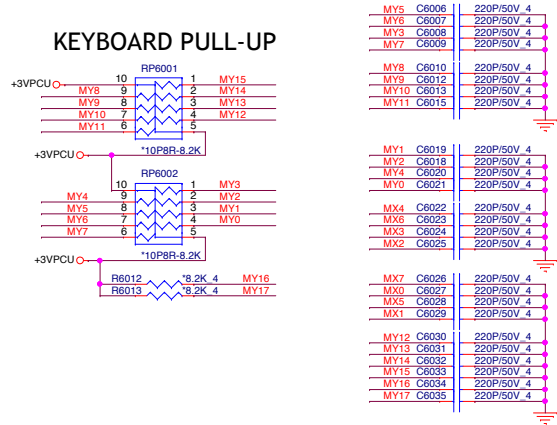
S




USB2
GB

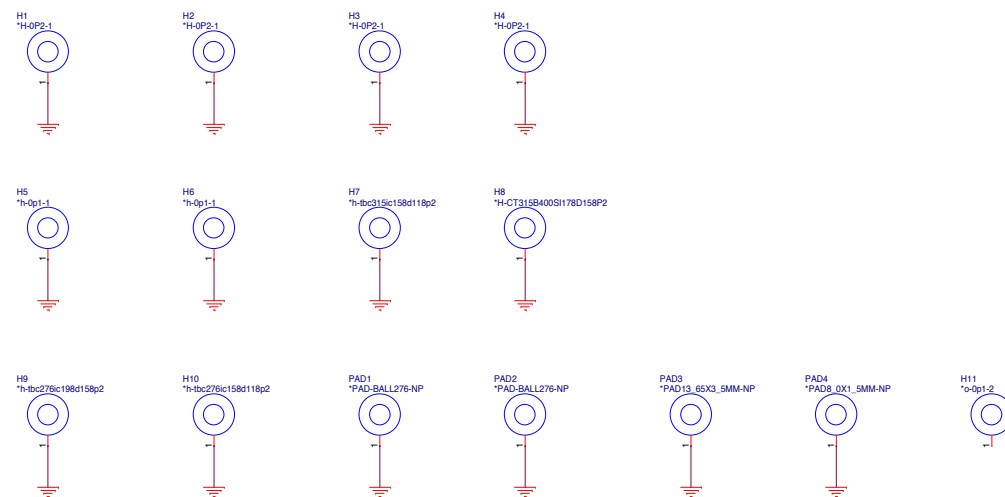


Power Button

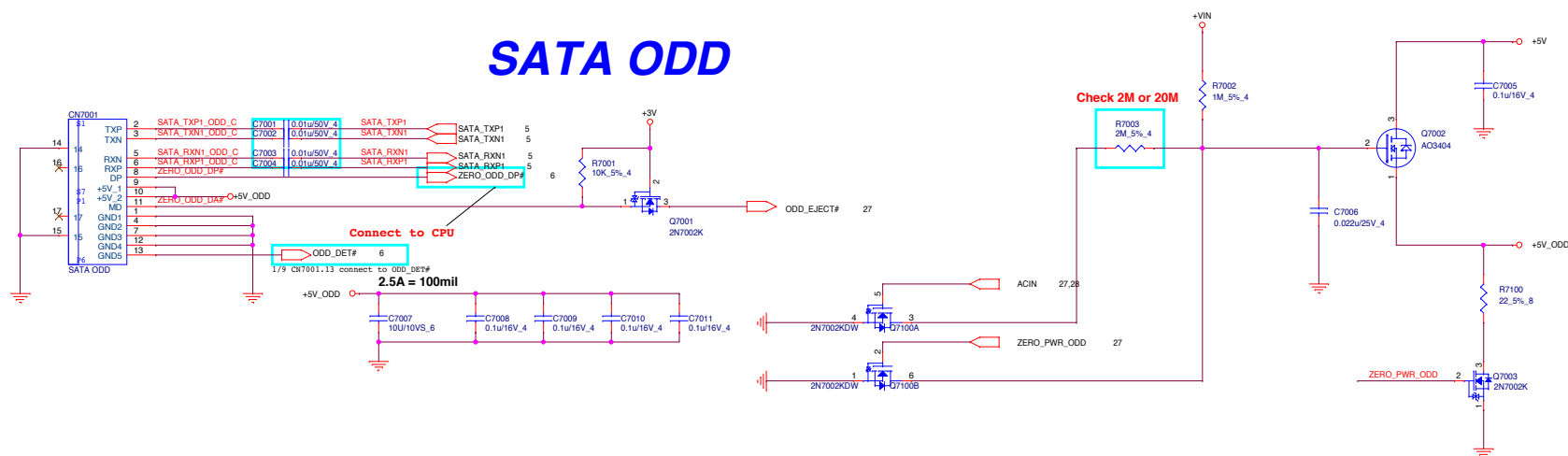


4,14..20,22..27,34,35	+3V	
8,10,20,24,26..29	+3VPCU	
15..18,23,24,34	+5V	
4,5,18,24,25,31	+1.8V	
34	+3VSUS	

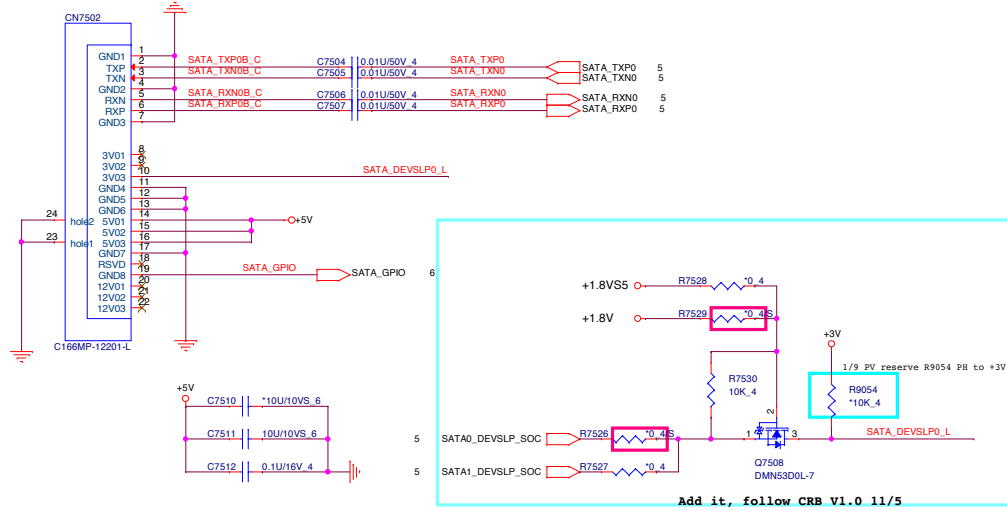
Hole



SATA ODD

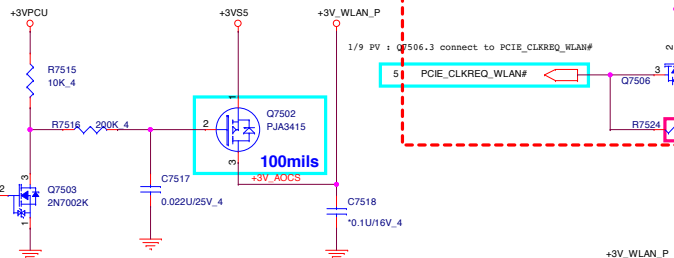


HDD
SATA HDD



WLAN

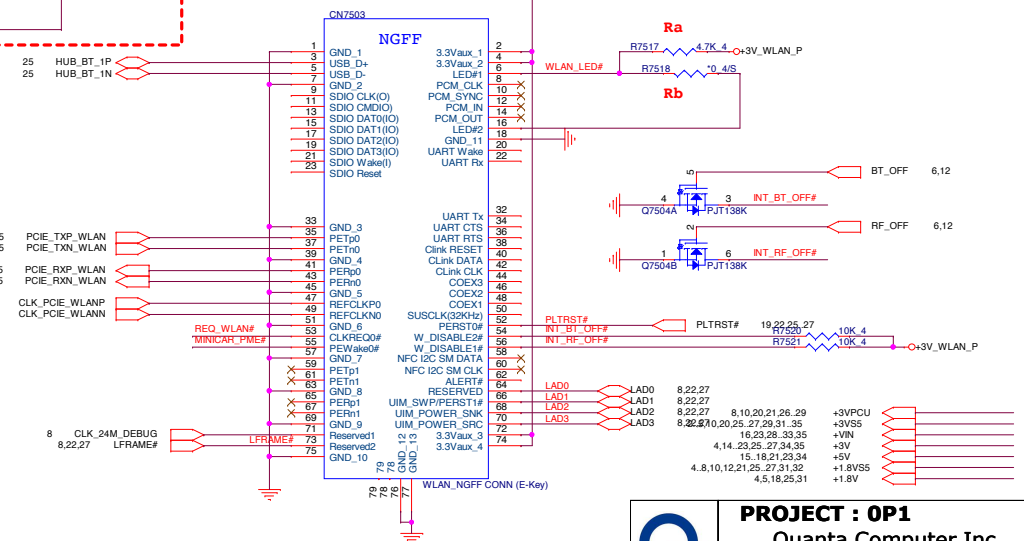
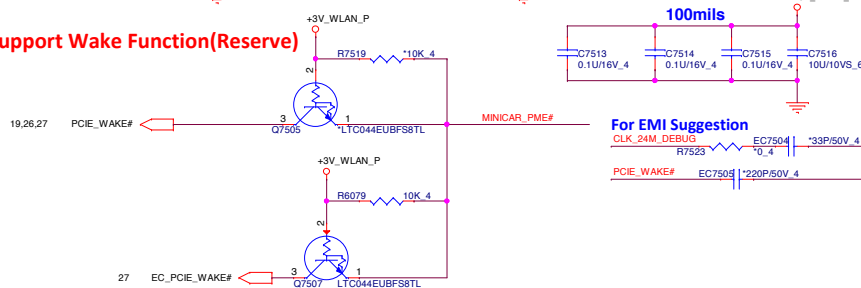
**Mini Card
WLAN/BT(Optional)**



```
0302 Reserved the MOSFET at CLKREQ#
even the current leakage test passed
for HP requested
```

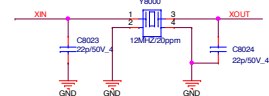
Remove Net RF_LINK# and need check if
Ra and Rb can be NI

Support Wake Function(Reserve)

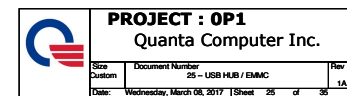


PROJECT : OP1
Quanta Computer Inc.

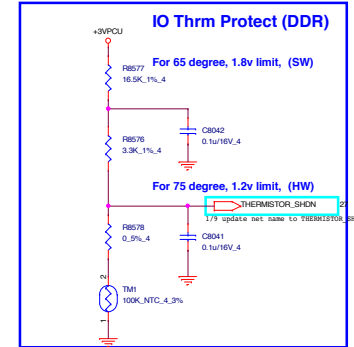
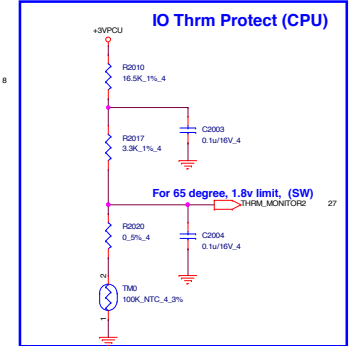
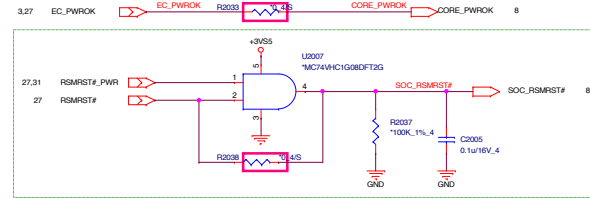
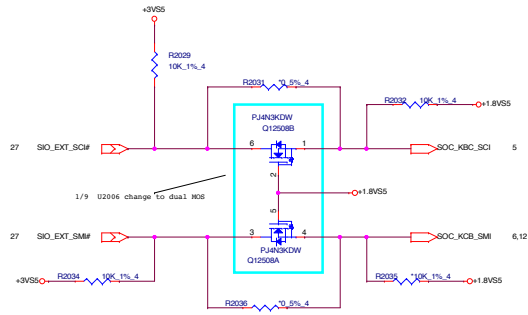
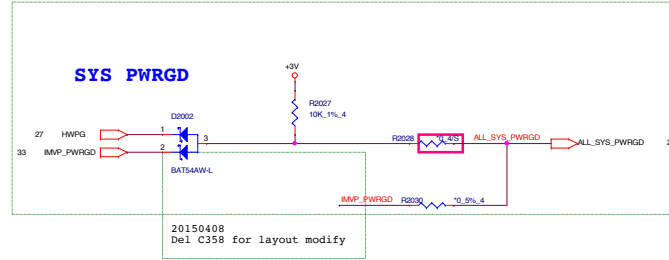
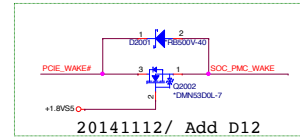
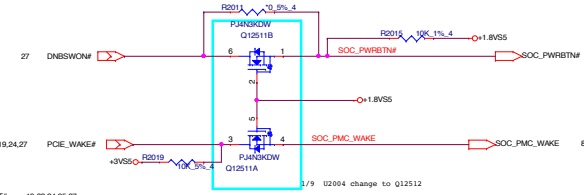
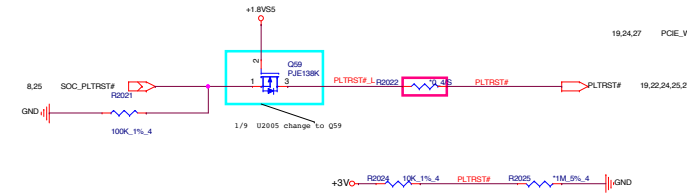
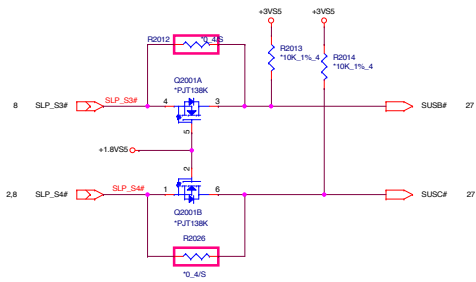
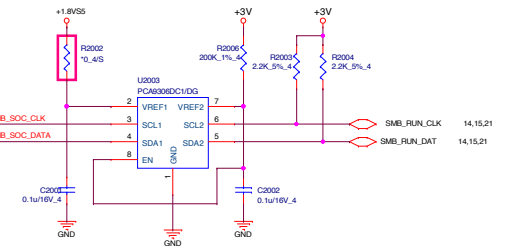
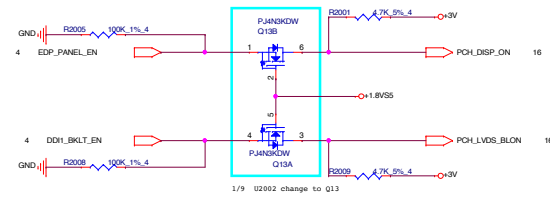
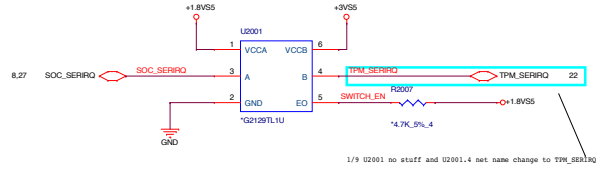
Size Custom	Document Number 24 -- HDD/WLAN(NGFF)	Rev 1A
Date:	Wednesday, March 08, 2017	Sheet 24 of 35



27741 - eMMC 32GB MO-276 MMCv5.0					
TOP50	QBCON	Description	SIZE	Vendor	
AKES3T2TW01	AKES3T2TW00	IC FLASH(153P) H26M4208EMR(FBGA) TOP B5Q	32G		Hynix
AKES3S2T501	AKES3S2T500	IC FLASH(153P) KLMBQ2JENB-0041 TOP B5Q	32G		Samsung
AKES3F-T107	AKES3F-T106	IC FLASH(153P) S01RADFA-JENB-010 TOP B5Q	32G		SanDisk
AKES3S2T001	AKES3S2T000	IC FLASH (153P) THGBMHG8C2LBAIL TOP B5Q	32G		Toshiba
27742 - eMMC 64GB MO-276 MMCv5.0					
AKES3T2TW03	AKES3T2TW02	IC FLASH(153P)H26M78208CMR(FBGA) TOP B5Q	64G		Hynix
AKES3ZF2T501	AKES3ZF2T500	IC FLASH(153P) KLMBG4JENB-010 TOP B5Q	64G		Samsung
AKES3ZPT014	AKES3ZPT013	IC FLASH(153P)THGBMHG9C4LBAIR TOP B5Q	64G		Toshiba



BOM : TPM SKU stuff U2001,R2007

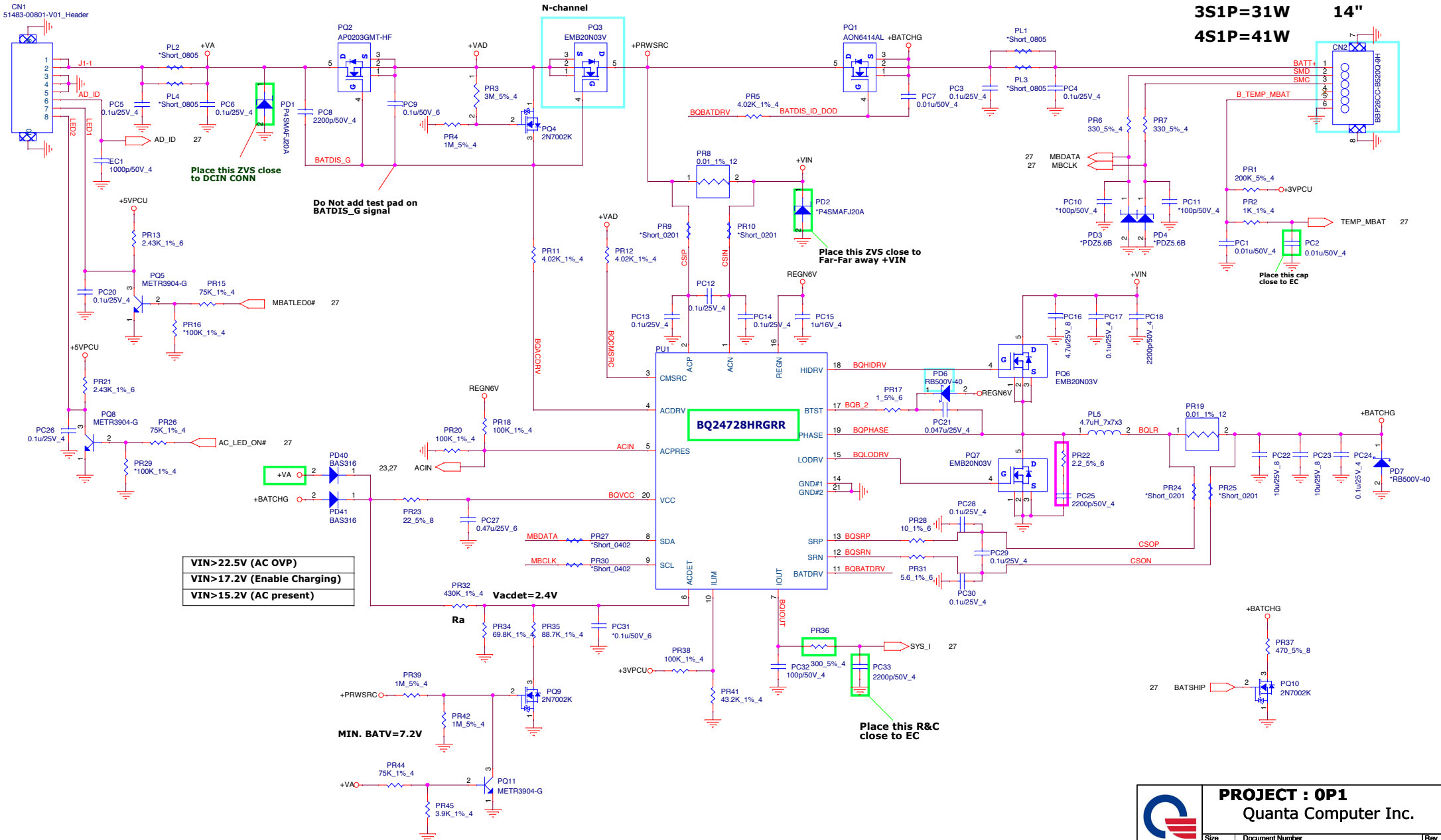


8,10,20,21,24,27,29	+3VPCU
2,5,10,20,24,25,27,29,31,35	+3V
4,8,10,12,21,24,25,27,31,32	+3VSS
4,5,18,24,25,31	+1.8V

PROJECT : OP1			
Quanta Computer Inc.			
Doc System	Document Number	26 - Level shift/Thermistor	Rev 1A
Date	Wednesday, March 08, 2017	Sheet	26 of 30



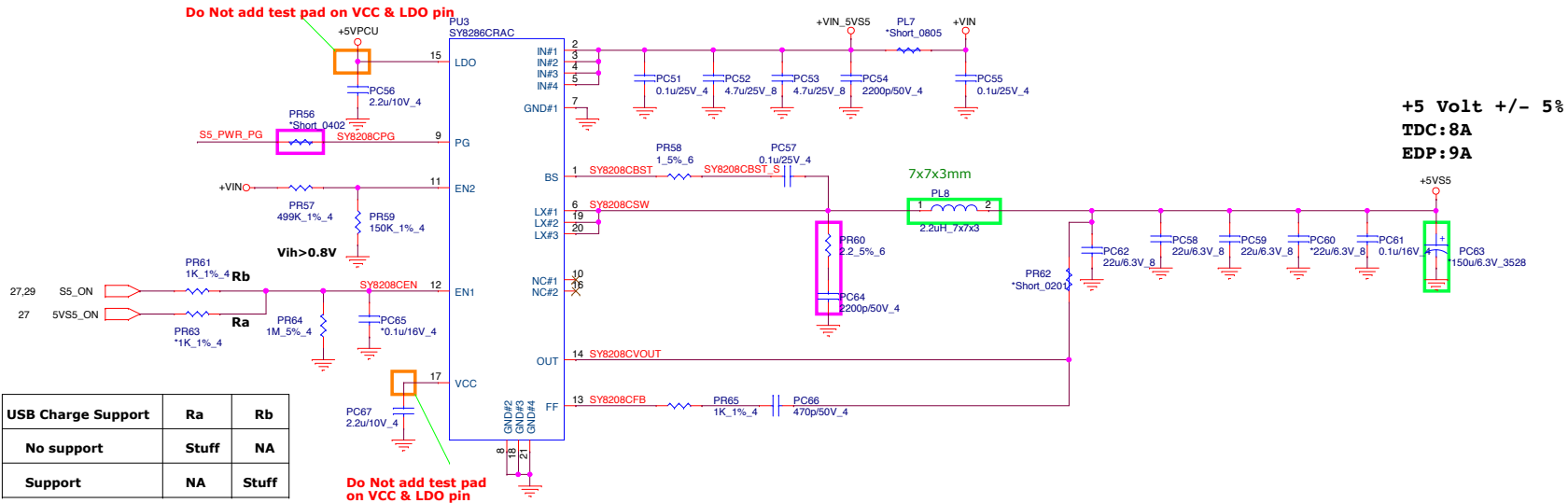
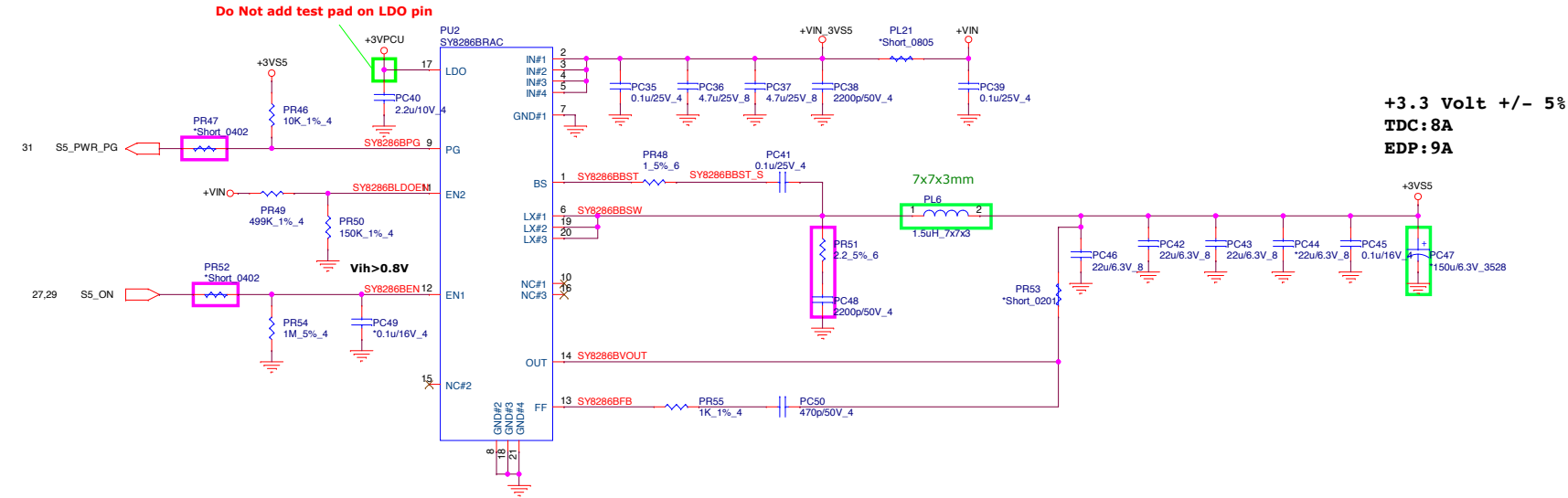
CN1
51483-00801-V01_Header



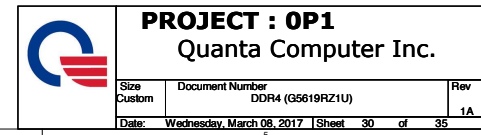
PROJECT : 0P1
Quanta Computer Inc.

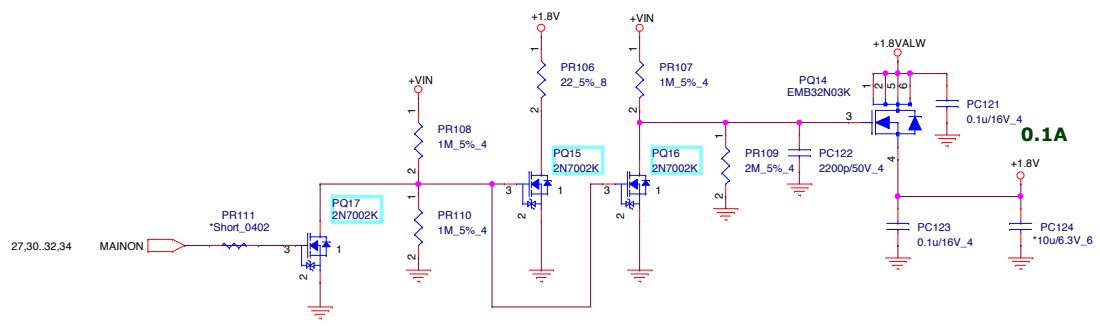
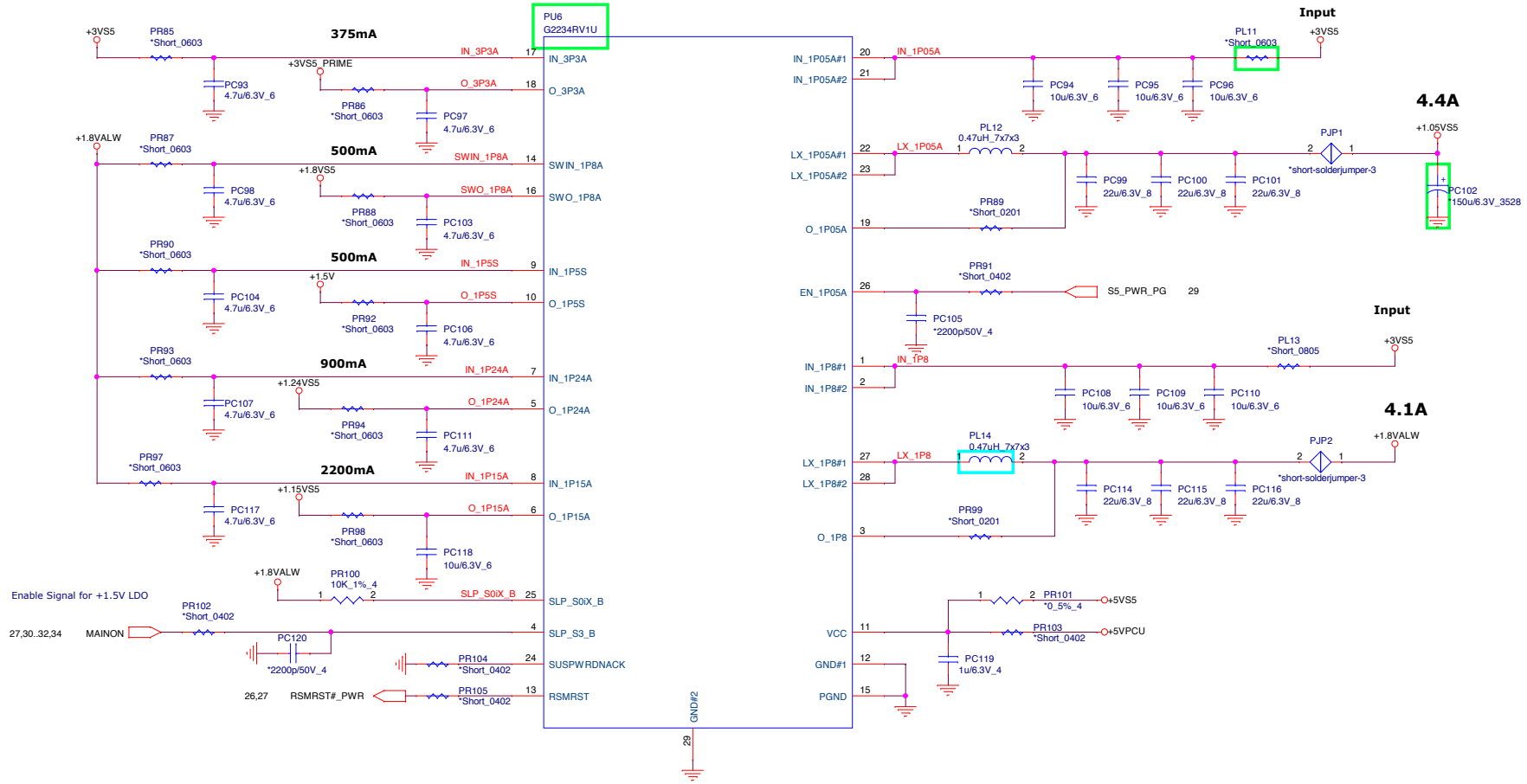
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+VIN	16,23,28,30,33,35
+3VS5	2,5,10,20,24,27,31,35
+5VS5	20,30,35
+3VPCU	8,10,20,21,24,26,28
+5VPCU	28,31,34



USB Charge Support	Ra	Rb
No support	Stuff	NA
Support	NA	Stuff

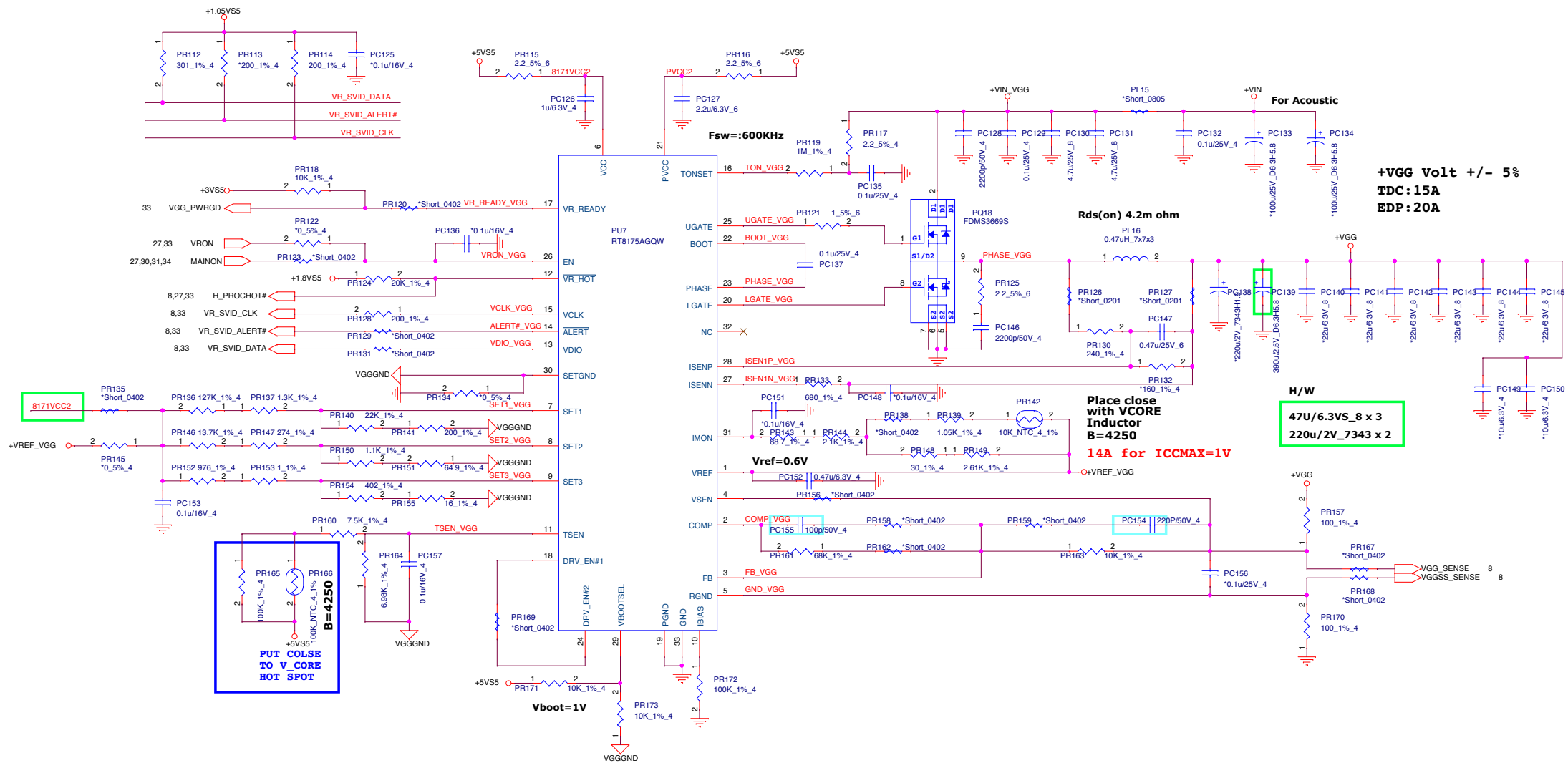




+3VS5	2,5,10,20,24,27,29,32,35
+1.8VALW	4,8,10,12,21,24,27,32
+3VS5_PRIME	10,27
+1.5V	10,17
+1.24VS5	10
+1.15VS5	9,31
+5VPCU	28,29,31,34
+1.05VS5	8,9,32,33
+1.15VS5	9,31
+5VPCU	28,29,31,34
+1.8V	4,5,18,24,25

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Custom	1.05VS5/1.8VS5/1.24VS5	1A
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+3V	4,14,27,35
+5V	15,18,21,23,24
+VIN	16,23,28,33,35
+3VS5	2,5,10,20,24,27,29,31,33,35
+5VS5	20,29,33,35
+3VSUS	21
+5VPCU	28,29,31
+3VLAVCC	19

